

Fig. 1

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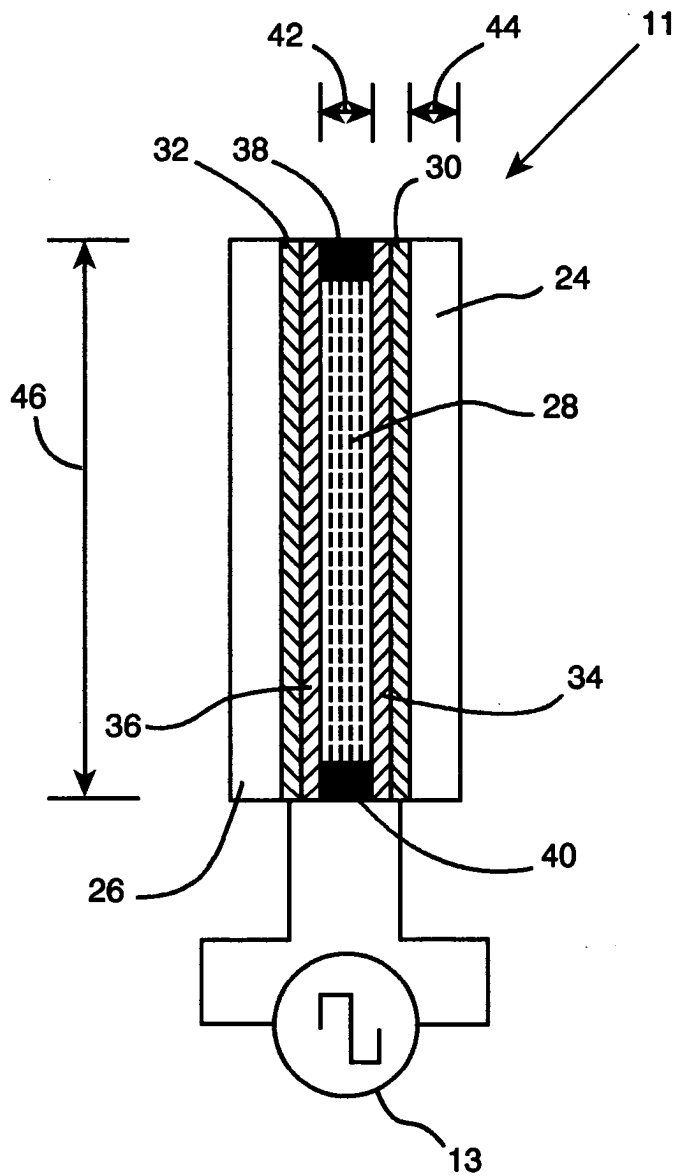


Fig. 2

3/12

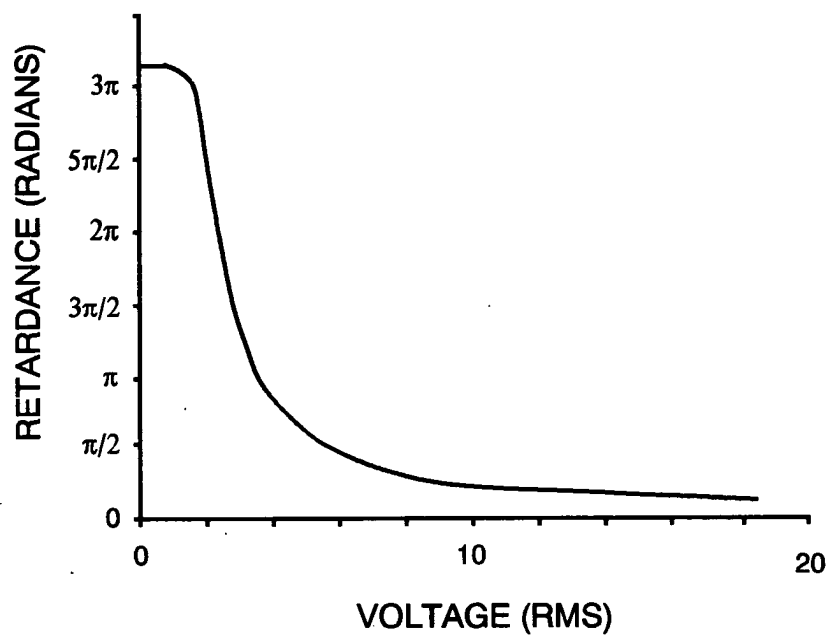


Fig. 3

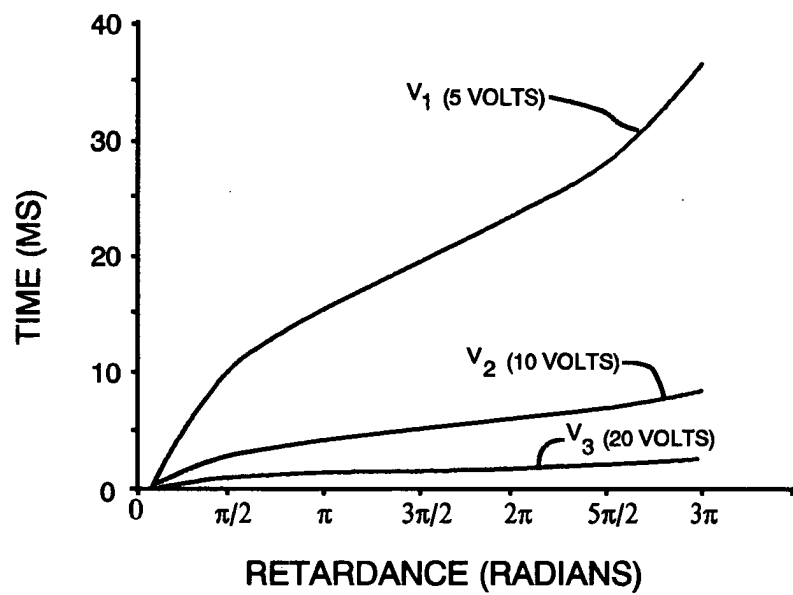


Fig. 4

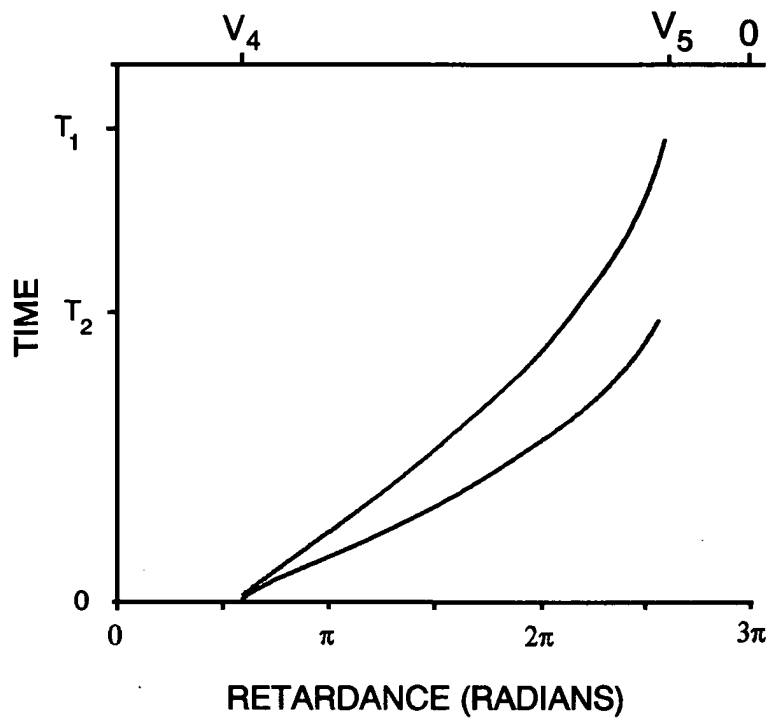


Fig. 5

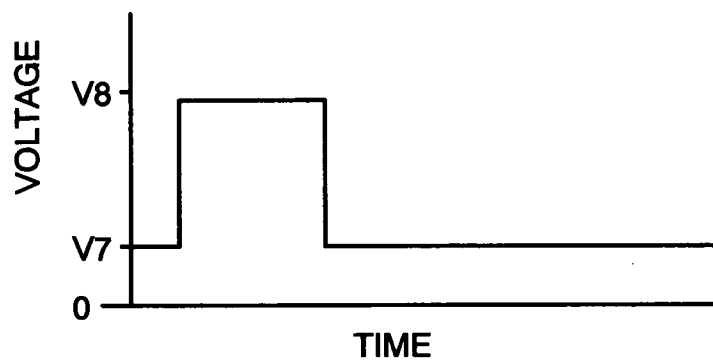


Fig. 6A

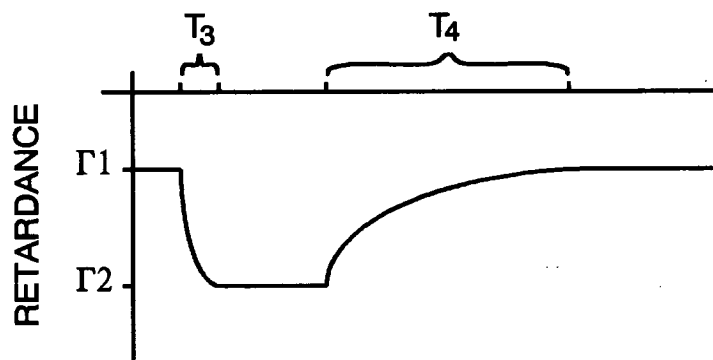


Fig. 6B

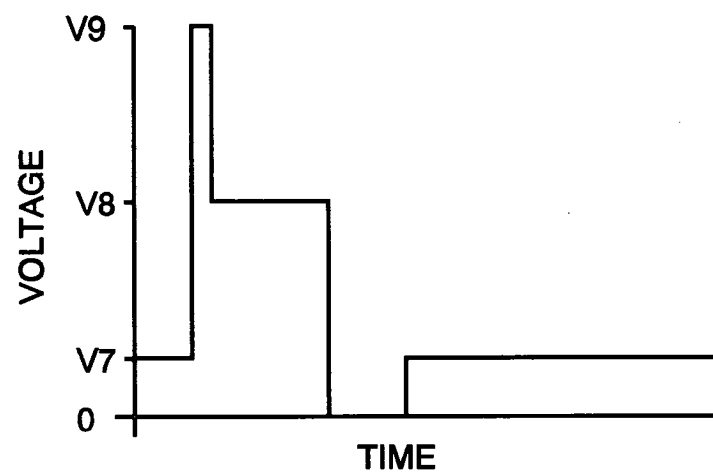


Fig. 6C

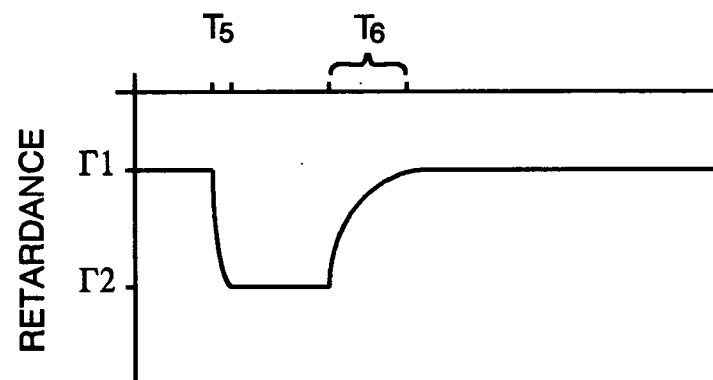


Fig. 6D

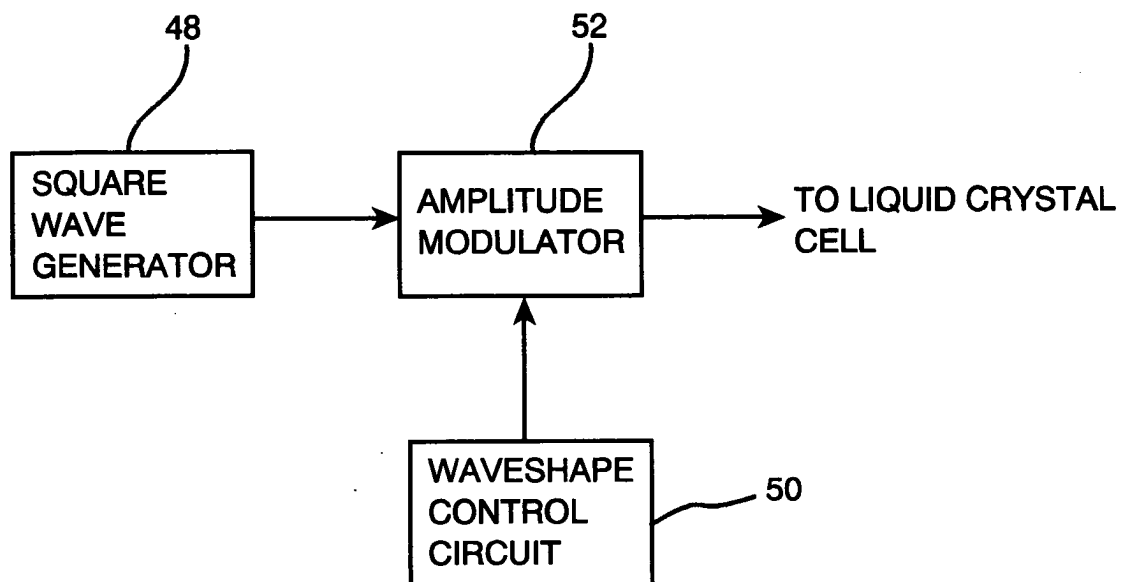


Fig. 7

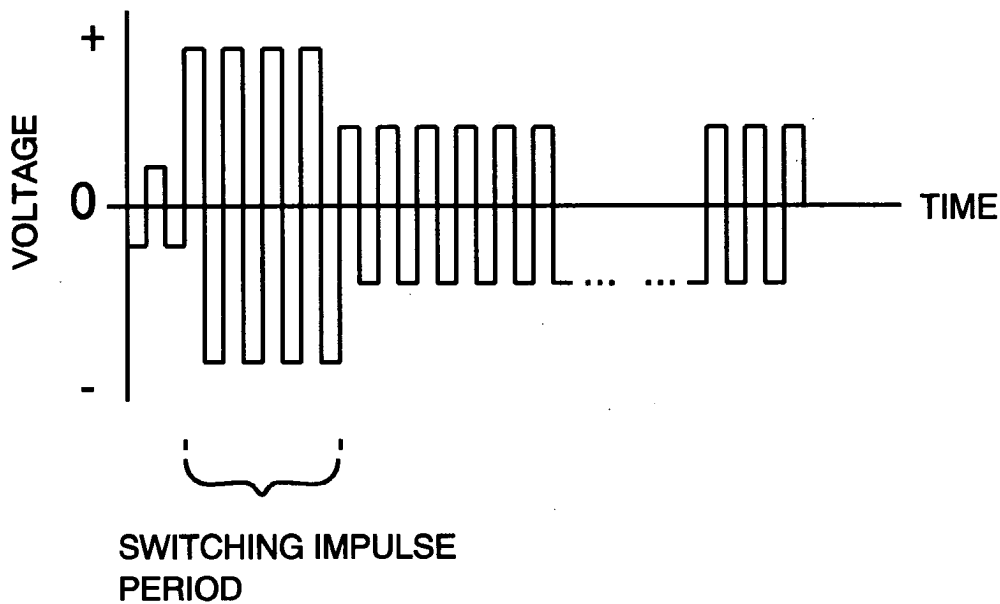


Fig. 8

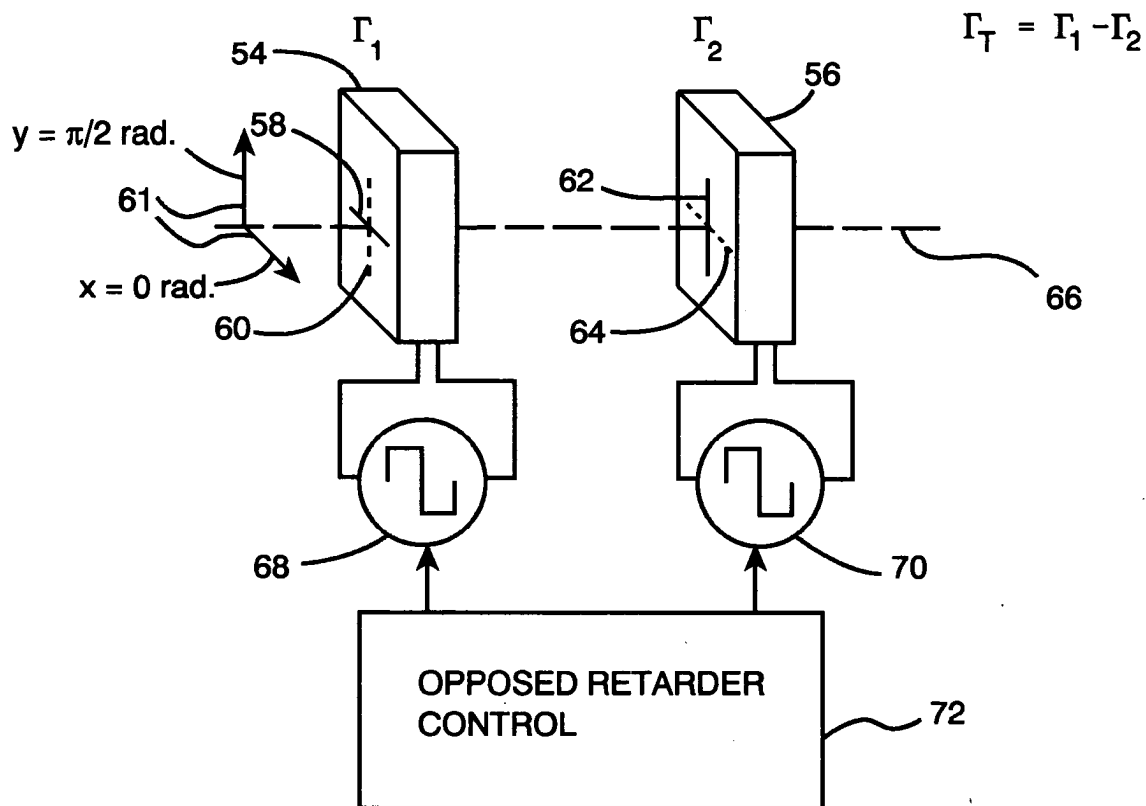


Fig. 9

DRIVE
VOLTAGE: 0 V_{bias} V_{max}
RETARDANCE: 2π π 0

Fig. 10A

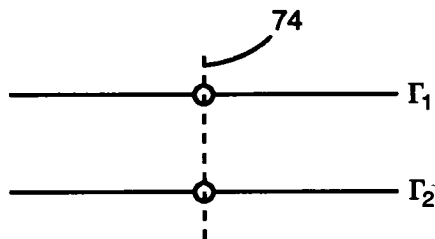


Fig. 10B

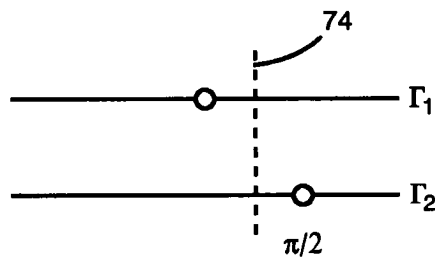


Fig. 10C

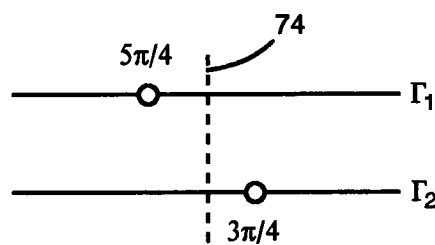


Fig. 10D

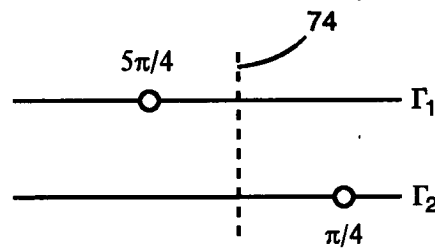
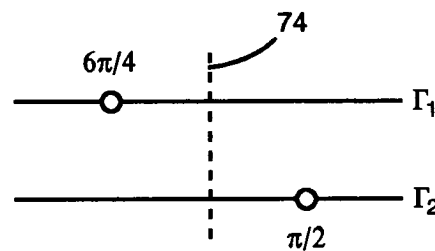


Fig. 10E



0 V_{bias} V_{max}
 2π π 0

Fig. 10F

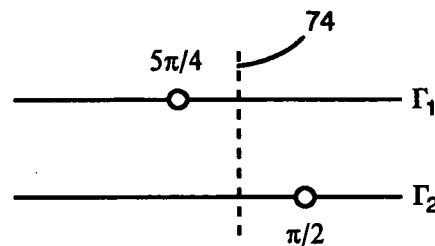


Fig. 10G

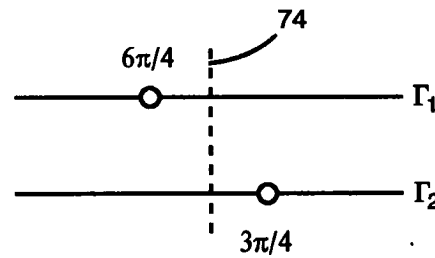


Fig. 10H

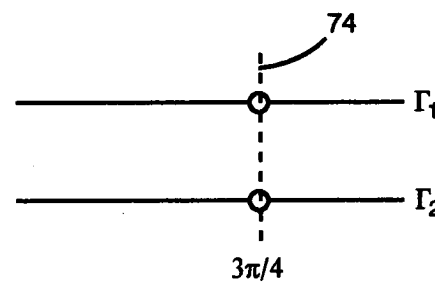
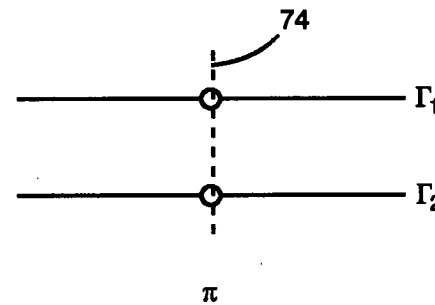


Fig. 10I



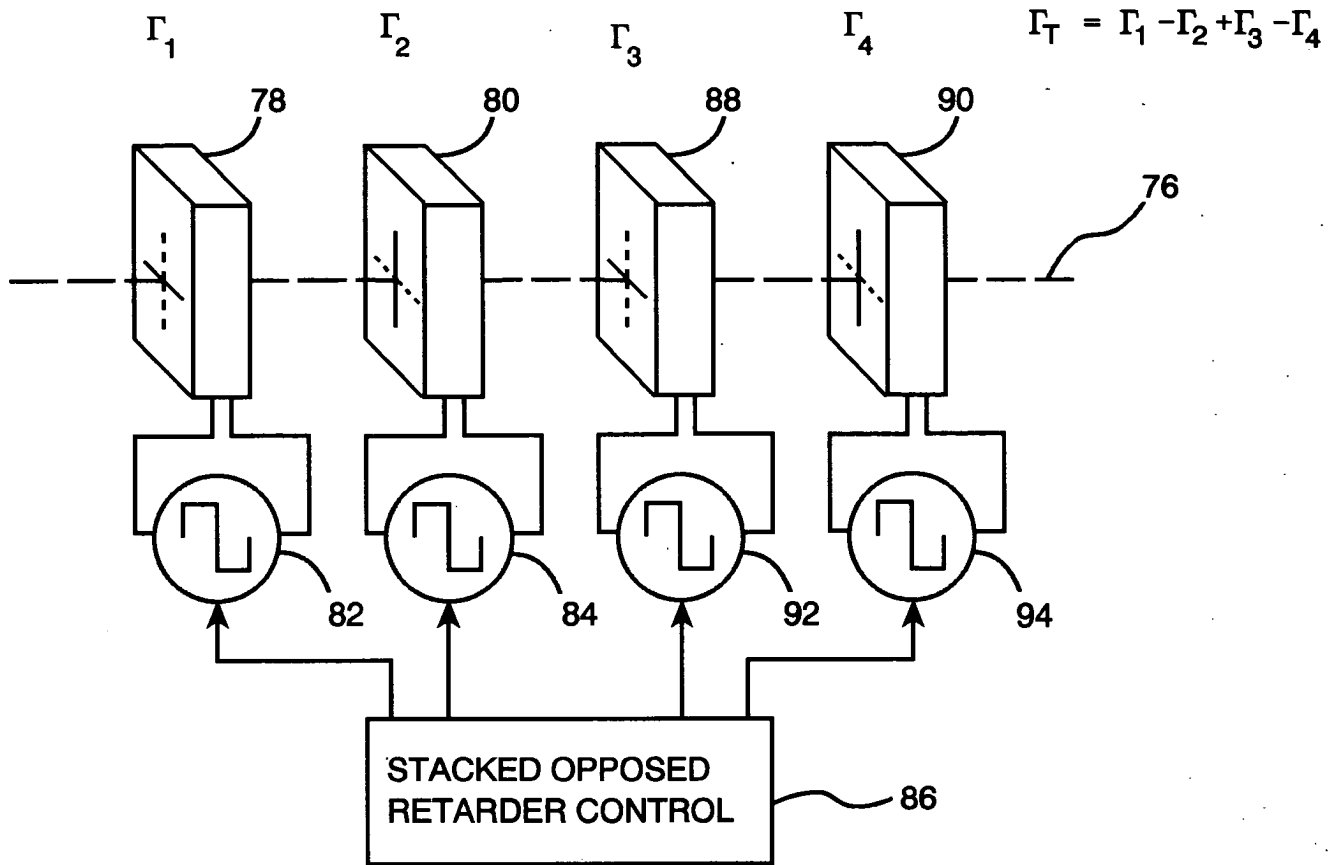


Fig.11